



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

PATENT

Application of:

Penilla, et al.

Application No: 09/430,350

Filed: October 29, 1999

For: METHOD FOR REDUCING NOISE IN  
INTEGRATED CIRCUIT LAYOUTS

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) Docket No: SUNMP099

) Group Art Unit: 2123

) Examiner: Jones, H.

) Date: May 14, 2002

#5 (Penilla)  
6-14-02  
Hall

CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service as First Class Mail to: Commissioner of Patents and Trademarks, Washington, DC 20231 on May 14, 2002.

Signed: \_\_\_\_\_

Neely Weldy

**COMMUNICATION REGARDING STATEMENT UNDER 37 C.F.R. §3.73(b)-ESTABLISHING  
RIGHT OF ASSIGNEE TO TAKE ACTION and POWER OF ATTORNEY BY ASSIGNEE  
(REVOCATION OF PRIOR POWERS)**


Commissioner for Patents  
Washington, D.C. 20231

Sir:

Applicant hereby attaches a Combined Statement Under 37 C.F.R. §3.73(b)-Establishing Right of Assignee to Take Action and Power of Attorney by Assignee of Entire Interest and Revocation of Prior Powers, and Appendix A, with reference to the above-identified U.S. patent application. Applicant respectfully requests this document be made of record.

Applicant believes that no fees are due in connection with this filing, however, if it is determined that any fees are due, the Commissioner is authorized to charge such fees to Deposit Account 50-0805 (SUNMP099). If the Commissioner has any questions concerning this document, please contact the undersigned at the telephone number set forth below.

Respectfully submitted,  
MARTINE & PENILLA, LLP

  
Albert S. Penilla, Esq.  
Reg. No. 39,487

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**IN THE UNITED STATES PATENT & TRADEMARK OFFICE**



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**COMBINED STATEMENT UNDER 37 C.F.R. § 3.73(b)-ESTABLISHING RIGHT OF ASSIGNEE  
TO TAKE ACTION  
AND  
POWER OF ATTORNEY BY ASSIGNEE OF ENTIRE INTEREST AND REVOCATION OF PRIOR  
POWERS**

Commissioner for Patents  
Washington, D.C. 20231

Sir:

**Statement Under 37 CFR §3.73(b):**

Sun Microsystems, Inc., a corporation organized under the laws of Delaware states that it is the assignee of entire right, title, and interest; in the patent(s) and/or patent application(s) listed on the attached APPENDIX A, by virtue of an Assignment from the inventor(s) of each of the patent(s) and/or patent application(s) listed on the attached APPENDIX A. Date of recordal, and Reel and Frame number of recordal by the United States Patent & Trademark Office identified on the attached APPENDIX A.

**Power of Attorney by Assignee of Entire Interest and Revocation of Prior Powers:**

Pursuant to 37 C.F.R. §1.36, Sun Microsystems, Inc., the record owner (per Statement Under 37 CFR §3.73(b) above) of the patent(s) and/or patent application(s) listed on the attached APPENDIX A, revokes all previous powers of attorney in the patent(s) and/or patent application(s) listed on the attached APPENDIX A and hereby appoints the following attorneys and/or agents assigned to the below Customer Number to prosecute the patent(s) and/or patent application(s) listed on the attached APPENDIX A and transact all business in the U.S. Patent and Trademark Office connected therewith:

**Customer Number 32,291**

Please send all correspondence to:

MARTINE & PENILLA, LLP  
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The undersigned (whose title is supplied below) is empowered to sign this certificate on behalf of the above-identified assignee.

The undersigned hereby declares that all statements made herein of the undersigned's own knowledge are true, and that all statements made on information and belief are believed to be true; and further, that these statements are made with the knowledge that willful false statements, and the like so made, are punishable by fine or imprisonment; or both, under Section 1001, Title 18 of the United States Code, and that such willful false statements may jeopardize the validity of the patents and patent applications listed on the attached APPENDIX A or any patents issuing thereon.



(Signature of authorized person)

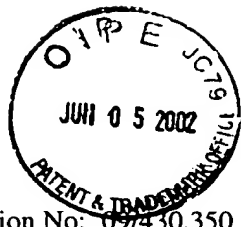
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Vice President Intellectual Property

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APPENDIX

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Patent Application No: 09/430,350  
Filing Date: October 29, 1999  
Applicant: SUTERA et al.  
Assignee: Sun Microsystems, Inc.  
Title: METHOD FOR REDUCING NOISE IN INTEGRATED CIRCUIT LAYOUTS  
**Recordation Date: January 19, 2000**  
**Reel/Frame: 010530/0704**  
Atty Docket: SUNMP099